

The Venue

Bydgoszcz is a city located in northern Poland, and a part of the metroplex Bydgoszcz-Toruń. With a city population of over 350,000 and an urban agglomeration with more than 470,000 inhabitants, Bydgoszcz is the 8th largest city in Poland. The oldest buildings in the city are grouped in the Old Town, just to mention: the Church of St Martin and Nicolaus, commonly known as Fara Church (a three-aisle late Gothic church erected between 1466 and 1502), the Church of the Assumption of the Holy Virgin (built between 1582–1602). The three granaries in Grodzka Street, picturesquely located on the Brda River in the immediate vicinity of the old Market Square, are the official symbol of the city. Among the most spectacular and atmospheric places in Bydgoszcz is Wyspa Młyńska (Mill Island), located just a few steps from the old Market Square. It was the 'industrial' centre of Bydgoszcz in the Middle Ages and for several hundred years thereafter, and it was here that the famous royal mint operated in the 17th century. The city is mostly associated with water, sports, Art Nouveau buildings, waterfront, music, and urban greenery. It is worth noting that it boasts the largest city park in Poland (830 ha). Bydgoszcz is the seat of Kazimierz Wielki University, University of Technology and Life Sciences, a very prestigious Academy of Music and a conservatory, hosts also the Filharmonia Pomorska concert hall, the Opera Nova opera house. Bydgoszcz is also an important place for contemporary European culture; one of the most important European centers of jazz music and The International Film Festival of the Art of Cinematography CAMERIMAGE.

<http://www.visitbydgoszcz.pl/en/explore/what-to-see>



Travel Arrangement

Air: Bydgoszcz I.J. Paderewski Airport is located in Bydgoszcz, about 3 km south from the city centre. Lufthansa, MistralAir and Ryanair serve the airport with connections to Birmingham, Dublin, Dusseldorf (Weeze), Frankfurt, Glasgow, London (Stansted) and Rome.

Rail: Several railway connections to Bydgoszcz from Warsaw, Poznań, Wrocław, Gdynia and other main towns and cities in Poland are available. The fast train trip to Warsaw takes about 3-3.5 hours.

Coach: Many coach connections are offered between Bydgoszcz and numerous cities in Poland. The coach trip from Warsaw takes about 4.5 hours.

Road: Bydgoszcz is located close to the main Polish roads no. 10 (Warsaw-Szczecin) and 25 (Wrocław-Koszalin). The main Polish north-south highway A1, E75 from Gdańsk to Cieszyn is located 60 km east from Bydgoszcz and easy accessible by car.

Accommodation

The conference will be held in a hotel located in the city centre. The accommodation will be offered for the Conference participants at conference site.

Deadlines

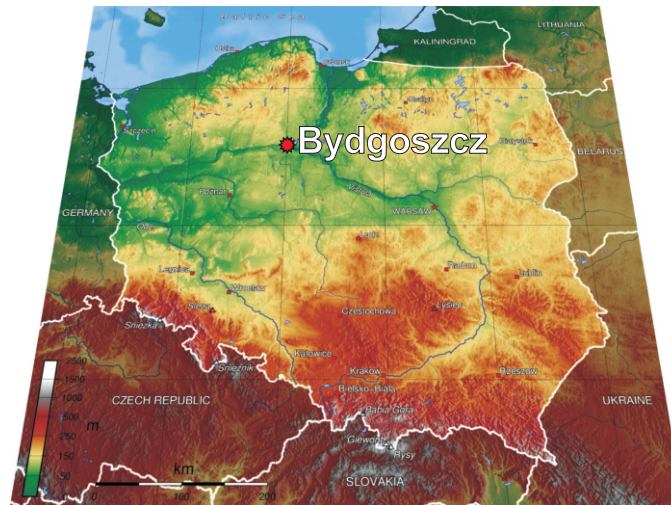
Intended authors should note the following deadlines:

| | |
|---|------------------------------------|
| Receipt of papers | March 1st, 2017 |
| Notification of acceptance of papers | April 30th, 2017 |
| Final paper versions and 1-page abstracts | May 31st, 2017 |

Price Information

| | |
|--|-----------------|
| Standard registration fee | 380 Euro |
| IEEE Members registration fee | 330 Euro |
| Student registration fee | 250 Euro |
| Late registration fee (paid after May 15 th , 2017) | 480 Euro |
| Accompanying person social events fee | 180 Euro |

Conference fee includes: participation in all sessions, MIXDES 2017 Proceedings CD and other conference materials, coffee and tea during the breaks, all lunches, the Welcome Party and Conference Banquet, tourist activities (student fee does not include Welcome Party and Conference Banquet). Conference fee does not include accommodation and transport.



Enquiries

Mariusz Orlikowski (Conference Secretary)
Lodz University of Technology
Department of Microelectronics and Computer Science (K-25)
ul. Wólczańska 221/223 (building B18), 90-924 Łódź, Poland
tel.: +48 604397239, fax: +48 426360327

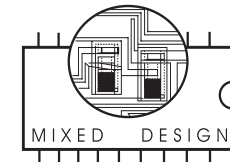
e-mail: mixdes2017@dmcs.p.lodz.pl

WWW

Information on MIXDES 2017 will be available on the World Wide Web at the following location:

<http://www.mixdes.org>

CALL FOR PAPERS



MIXDES 2017

24th International Conference MIXED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS



**An invitation to submit paper proposals
and attend the conference**



Bydgoszcz, POLAND, 22 - 24 June 2017



MIXDES 2017
24th International Conference
MIXED DESIGN OF INTEGRATED
CIRCUITS AND SYSTEMS
Bydgoszcz, Poland, 22 - 24 June 2017

Organized by:

Department of Microelectronics and Computer Science,
Lodz University of Technology, Poland
Institute of Micro- and Optoelectronics
Warsaw University of Technology, Poland

In cooperation with:

Poland Section IEEE - CAS & ED Chapters
Section of Microelectronics
and Section of Signals, Electronic Circuits and Systems
of the Committee of Electronics and Telecommunication
of the Polish Academy of Sciences
Commission of Electronics and Fotonics
of Polish National Committee
of International Union of Radio Science - URSI

International Programme Committee

| | |
|-----------------------------|--|
| Prof. M. Adamski | Technical University of Zielona Góra, Poland |
| Prof. M. Bucher | Technical University of Crete, Greece |
| Prof. J. Cabestany | Universitat Politècnica de Catalunya, Spain |
| Prof. Z. Ciota | Lodz University of Technology, Poland |
| Prof. J. Collet | LAAS - CNRS, Toulouse, France |
| Prof. A. Dąbrowski | Poznan University of Technology, Poland |
| Prof. G. De Mey | University of Ghent, Belgium |
| (Vice-Chairman) | |
| Prof. J. Deen | McMaster University, Canada |
| Prof. A. Filipkowski | Warsaw University of Technology, Poland |
| Dr. D. Foty | Gilgamesh Associates, USA |
| Prof. M. Glesner | Technische Hochschule Darmstadt, Germany |
| Prof. L. Golonka | Wroclaw University of Science and Techn., Poland |
| Dr. W. Grabiński | GMC, Switzerland |
| Prof. P. Gryboś | AGH University of Science and Technology, Poland |
| Prof. V. Hahanov | Kharkiv National University of Radioelectronics, Ukraine |
| Prof. A. Handkiewicz | Poznan University of Technology, Poland |
| Prof. A. Hatzopoulos | Aristotle University of Thessaloniki, Greece |
| Dr. S. Hausman | Lodz University of Technology, Poland |
| Dr. G. Jabłoński | Lodz University of Technology, Poland |
| Prof. A. Jakubowski | Warsaw University of Technology, Poland |
| Prof. A. Kobus | Institute of Electron Technology, Warsaw, Poland |
| Prof. A. Kos | AGH University of Science and Technology, Poland |
| Prof. W. Kuźmicz | Warsaw University of Technology, Poland |
| (Programme Chairman) | |
| Prof. M. Lobur | Lviv Polytechnical National University, Ukraine |
| Dr. M.M. Louerat | Université Pierre et Marie Curie, Paris, France |
| Prof. T. Łuba | Warsaw University of Technology, Poland |
| Prof. B. Macukow | Warsaw University of Technology, Poland |
| Prof. J. Madrenas | Universitat Politècnica de Catalunya, Spain |

| | |
|----------------------------|--|
| Prof. A. Martinez | LAAS - CNRS, Toulouse, France |
| (Honorary Chairman) | |
| Prof. A. Materka | Lodz University of Technology, Poland |
| Prof. W. Mathis | Leibniz University of Hannover, Germany |
| Prof. J.M. Moreno | Universitat Politècnica de Catalunya, Spain |
| Dr. M. Napieralska | Lodz University of Technology, Poland |
| Prof. A. Napieralski | Lodz University of Technology, Poland |
| (General Chairman) | |
| Prof. J. Nishizawa | Semiconductor Research Institute, Japan |
| Dr. J.L. Noullet | Chipyards, France |
| Prof. M. Ogorzałek | AGH University of Science and Technology, Poland |
| Prof. L. Opalski | Warsaw University of Technology, Poland |
| Prof. A. Pfitzner | Warsaw University of Technology, Poland |
| Prof. E. Piętko | Silesian University of Technology, Poland |
| Prof. W. Pleskacz | Warsaw University of Technology, Poland |
| Dr. B.F. Romanowicz | Nano Science and Technology Institute, USA |
| Prof. J.A. Rubio | Universitat Politècnica de Catalunya, Spain |
| Prof. J. Rutkowski | Silesian University of Technology, Poland |
| Prof. A. Rybarczyk | Poznan University of Technology, Poland |
| Dr. J.-M. Sallese | Swiss Federal Institute of Technology, Switzerland |
| Prof. D. Sankowski | Lodz University of Technology, Poland |
| Dr. M. Schwarz | Robert Bosch GmbH, Germany |
| Prof. N. Stojadinović | University of Niš, Serbia |
| Prof. V. Székely | Technical University of Budapest, Hungary |
| Prof. T. Szmuc | AGH University of Science and Technology, Poland |
| Prof. M. Tadeusiewicz | Lodz University of Technology, Poland |
| Dr. D. Tomaszewski | Institute of Electron Technology, Warsaw, Poland |
| Dr. P. Tounsi | INSA de Toulouse, France |
| Dr. M. Turowski | Silvaco Inc., USA |
| Prof. R. Ubar | Tallin Technical University, Estonia |
| Prof. G. Wachutka | Technische Universität München, Germany |
| Prof. K. Wawryn | Technical University of Koszalin, Poland |
| Prof. B. Więcek | Lodz University of Technology, Poland |
| Prof. S. Yoshitomi | Toshiba Corp., Japan |
| Prof. J. Zarębski | Gdynia Maritime University, Poland |
| Prof. M. Zubert | Lodz University of Technology, Poland |

Organizing Committee

| | |
|---|---|
| Prof. A. Napieralski | (Chairman) |
| Dr. M. Orlikowski | (Secretary) |
| Dr. M. Napieralska | (Vice-Chairman) |
| Dr. G. Jabłoński | Department of Microelectronics and Computer Science |
| Dr. M. Piotrowicz | Lodz University of Technology, Poland |
| Prof. W. Kuźmicz | |
| Institute of Micro- and Optoelectronics | |
| Warsaw University of Technology, Poland | |

The MIXDES Conference

The MIXDES Conference series started in Dębe near Warsaw in 1994 and has been organised yearly in different Polish cities. This time we would like to continue the tradition of inviting you to the most attractive places in Poland and the MIXDES 2017 will take place in Bydgoszcz.

In short period of time the conference has become an important event in the Central Europe allowing to discuss the recent research progress in the field of design, modelling, simulation, testing and manufacturing in various areas such as micro- and nanoelectronics, semiconductors, sensors, actuators and power devices as well as their interdisciplinary applications.

Areas of Interest

The topics of the Conference include:

- 1. Design of Integrated Circuits and Microsystems**
Design methodologies. Digital and analog synthesis. Hardware-software codesign. Reconfigurable hardware. Hardware description languages. Intellectual property-based design. Design reuse.
- 2. Thermal Issues in Microelectronics**
Thermal and electro-thermal modelling, simulation methods and tools. Thermal mapping. Thermal protection circuits.
- 3. Analysis and Modelling of ICs and Microsystems**
Simulation methods and algorithms. Behavioural modelling with VHDL-AMS and other advanced modelling languages. Microsystems modelling. Model reduction. Parameter identification.
- 4. Microelectronics Technology and Packaging**
New microelectronic technologies. Packaging. Sensors and actuators.
- 5. Testing and Reliability**
Design for testability and manufacturability. Measurement instruments and techniques.
- 6. Power Electronics**
Design, manufacturing, and simulation of power semiconductor devices. Hybrid and monolithic Smart Power circuits. Power integration.
- 7. Signal Processing**
Digital and analogue filters, telecommunication circuits. Neural networks. Artificial intelligence. Fuzzy logic. Low voltage and low power solutions.
- 8. Embedded Systems**
Design, verification and applications.
- 9. Medical Applications**
Medical and biotechnology applications. Biometrics. Thermography in medicine.

Official Language

Official language of the Conference is English, neither translation nor interpretation will be provided.

Call for Papers and Contributions

A call is made for papers, contributions and other conference activities on the topics mentioned above.

Full papers and 1-page abstracts should be submitted to the Organizing Committee till March 1st, 2017 - only in electronic form (MS Word, RTF, Open Office Writer, LaTeX, together with a generated PDF file).

The required format is available on our Web page. Authors are asked to indicate the topic into which their papers fall. Papers will be reviewed by at least two referees from the International Programme Committee and the accepted papers will be printed in the proceedings from the author's electronic submission.

Tutorials and Special Sessions - Call for Proposals

Several tutorials/special sessions will be held prior to the conference. Authors willing to propose a tutorial at MIXDES 2017 are invited to send a proposal to the Organizing Committee. The proposal should consist of a summary including tutorial title, name and affiliation of the lecturer(s), tutorial objectives and audience, topical outline and provisional schedule of the tutorial.