

The Venue

We would like to invite you to Gdynia, the town which is an important seaport at Gdańsk Bay located on the south coast of the Baltic Sea. Along with two other cities, Sopot and Gdańsk, Gdynia forms a metropolitan area called the Tri-City. Gdynia born out of the Treaty of Versailles in 1919. Even if it is one the youngest Polish cities, the first reference to the village was given to the Order of Norbertan Sisters from Żukowo dates back to 1209, so it possesses a few historical monuments including the Church of Saint Michael Archangel in Oksywie built in 1224.

The port of Gdynia is the gateway to Poland and a landmark of city. It ranks among the best in the Baltic region, as it is not only a cargo-handling facility but also an international business centre.

The main attractions in Gdynia are the museum ships Dar Pomorza and Błyskawica, which are located along the pier, the Gdynia Aquarium and the recently built magnificent Sea Towers. Dar Pomorza, built in 1930, was recognised as the Most Beautiful White Frigate, and it will be a place of celebration the 25th anniversary of MIXDES conferences. For those who are not interested in ships there are wonderful opportunities to watch wildlife in the Sea Aquarium or nature reserve Redłowska Kępa.

Today Gdynia has more than 250 000 inhabitants and it is a modern and dynamically-growing city with its modern development and economy open towards the sea.

For more information see:

<http://guide.trojmiasto.pl>, http://www.staypoland.com/about_gdynia.htm



Travel Arrangement

Air: The airport, Port Lotniczy Gdańsk im. Lecha Wałęsy (Gdańsk Lech Wałęsa airport), is 20 km south of Gdynia centre in the Gdańsk Rębiechowo district. A train service connects the airport with the three cities of Gdańsk, Sopot and Gdynia.

Rail: Gdynia is connected to all major cities in Poland. See <http://rozklad.pkp.pl/en> for most up-to-date connection schedule.

Coach: Many travel agencies offer permanent coach connections between Gdynia and numerous cities in Europe.

Road: There are three main routes leading directly to Gdańsk, Sopot and Gdynia: the E28 from the west, the E75 from the south, and the E77 from the east.

Accommodation

The conference will be held in a hotel located in the city centre. The accommodation will be offered for the Conference participants at the conference site.

Deadlines

Intended authors should note the following deadlines:

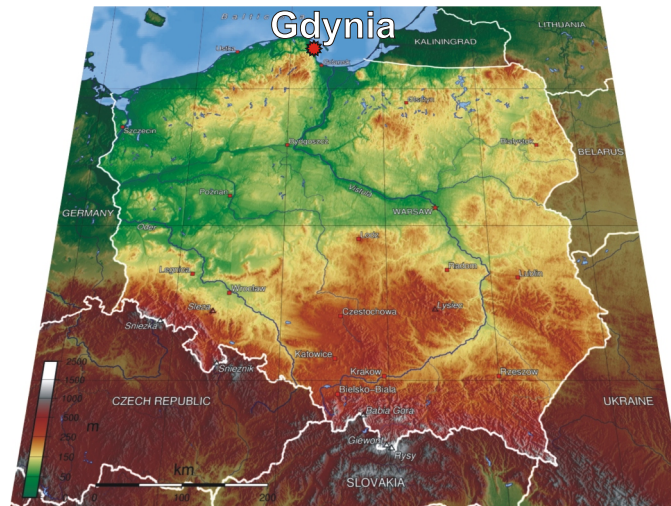
| | |
|---|----------------------------------|
| Receipt of papers | February 28 th , 2018 |
| Notification of acceptance of papers | April 30 th , 2018 |
| Final paper versions and 1-page abstracts | May 15 th , 2018 |

Price Information

| | |
|--|----------|
| Standard registration fee | 360 Euro |
| IEEE Members registration fee | 300 Euro |
| Student registration fee | 260 Euro |
| Late registration fee (paid after May 15 th , 2018) | 450 Euro |
| Accompanying person social events fee | 150 Euro |

Conference fee includes: participation in all sessions, MIXDES 2018 Proceedings CD and other conference materials, coffee and tea during the breaks, all lunches, the Welcome Party and Conference Banquet, tourist activities (student fee does not include welcome party and closing ceremony).

Conference fee does not include accommodation and transport.



Enquiries

Mariusz Orlikowski (Conference Secretary)
Technical University of Łódź
Department of Microelectronics and Computer Science (K-25)
ul. Wólczańska 221/223 (building B18), 90-924 Łódź, Poland
tel.: +48 604397239, fax: +48 426360327

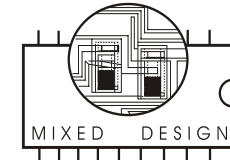
e-mail: mixdes2018@dmcs.p.lodz.pl

WWW

Information on MIXDES 2018 will be available on the World Wide Web at the following location:

<http://www.mixdes.org>

CALL FOR PAPERS



MIXDES 2018

25th International Conference MIXED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS



**An invitation to submit paper proposals
and attend the Conference**



Gdynia, POLAND, 21 - 23 June 2018



MIXDES 2018 **25th International Conference** **MIXED DESIGN OF INTEGRATED** **CIRCUITS AND SYSTEMS** **Gdynia, POLAND, 21-23 June 2018**

Organized by:

Department of Microelectronics and Computer Science,
Technical University of Lodz, Poland
Department of Marine Electronics,
Gdynia Maritime University, Poland
Institute of Micro- and Optoelectronics
Warsaw University of Technology, Poland

Under the auspices of:

Rector of Gdynia Maritime University, Poland

In cooperation with:

Poland Section IEEE - CAS & ED Chapters
Section of Microelectronics and Electron Technology
of the Committee of Electronics and Telecommunication
of the Polish Academy of Sciences
Commission of Electronics and Photonics
of Polish National Committee
of International Union of Radio Science - URSI

International Programme Committee

| | |
|-----------------------------|---|
| Prof. M. Adamski | Technical University of Zielona Góra, Poland |
| Prof. M. Bucher | Technical University of Crete, Greece |
| Prof. J. Cabestany | Universitat Politècnica de Catalunya, Spain |
| Prof. Z. Ciota | Technical University of Lodz, Poland |
| Prof. J. Collet | LAAS - CNRS, Toulouse, France |
| Prof. A. Dąbrowski | Poznan University of Technology, Poland |
| Prof. G. De Mey | University of Ghent, Belgium |
| (Vice-Chairman) | |
| Prof. J. Deen | McMaster University, Canada |
| Prof. M.H. Fino | Universidade Nova de Lisboa, Portugal |
| Dr. D. Foty | Gilgamesh Associates, USA |
| Prof. M. Glesner | Technische Hochschule Darmstadt, Germany |
| Prof. L. Golonka | Wrocław University of Technology, Poland |
| Prof. K. Górecki | Gdynia Maritime University, Poland |
| Dr. W. Grabiński | GMC, Switzerland |
| Prof. P. Gryboś | AGH University of Science and Technology, Poland |
| Prof. V. Hahanov | Kharkiv National Univ. of Radioelectronics, Ukraine |
| Prof. A. Handkiewicz | Poznan University of Technology, Poland |
| Prof. A. Hatzopoulos | Aristotle University of Thessaloniki, Greece |
| Dr. S. Hausman | Technical University of Lodz, Poland |
| Dr. G. Jabłoński | Lodz University of Technology, Poland |
| Prof. A. Jakubowski | Warsaw University of Technology, Poland |
| Prof. A. Kos | AGH University of Science and Technology, Poland |
| Prof. W. Kuźmicz | Warsaw University of Technology, Poland |
| (Programme Chairman) | |
| Prof. C. Lallement | Strasbourg University, France |
| Prof. M. Lobur | Lviv Polytechnical National University, Ukraine |
| Dr. M.M. Louerat | Université Pierre et Marie Curie, Paris, France |
| Prof. T. Łuba | Warsaw University of Technology, Poland |
| Prof. B. Macukow | Warsaw University of Technology, Poland |
| Prof. J. Madrenas | Universitat Politècnica de Catalunya, Spain |

| | |
|----------------------------|--|
| Prof. A. Martinez | LAAS - CNRS, Toulouse, France |
| (Honorary Chairman) | |
| Prof. A. Materka | Technical University of Lodz, Poland |
| Prof. W. Mathis | University of Magdeburg, Germany |
| Prof. J.M. Moreno | Universitat Politècnica de Catalunya, Spain |
| Dr. M. Napieralska | Technical University of Lodz, Poland |
| Prof. A. Napieralski | Technical University of Lodz, Poland |
| (General Chairman) | |
| Prof. J. Nishizawa | Semiconductor Research Institute, Japan |
| Dr. J.L. Noullet | INSA de Toulouse, France |
| Prof. M. Ogorzałek | AGH University of Science and Technology, Poland |
| Prof. L. Opalski | Warsaw University of Technology, Poland |
| Prof. A. Pfitzner | Warsaw University of Technology, Poland |
| Prof. E. Piętko | Silesian University of Technology, Poland |
| Prof. W. Pleskacz | Warsaw University of Technology, Poland |
| Dr. B.F. Romanowicz | Nano Science and Technology Institute, USA |
| Prof. J.A. Rubio | Universitat Politècnica de Catalunya, Spain |
| Prof. J. Rutkowski | Silesian University of Technology, Poland |
| Prof. A. Rybarczyk | Poznan University of Technology, Poland |
| Dr. J.-M. Sallèse | Swiss Federal Institute of Technology, Switzerland |
| Prof. D. Sankowski | Technical University of Lodz, Poland |
| Dr. M. Schwarz | Robert Bosch GmbH, Germany |
| Prof. N. Stojadinović | University of Niš, Serbia |
| Prof. V. Székely | Technical University of Budapest, Hungary |
| Prof. T. Szmuc | AGH University of Science and Technology, Poland |
| Dr. P. Śniatała | Poznan University of Technology, Poland |
| Prof. M. Tadeusiewicz | Technical University of Lodz, Poland |
| Dr. D. Tomaszewski | Institute of Electron Technology, Warsaw, Poland |
| Dr. P. Tounsi | INSA de Toulouse, France |
| Dr. M. Turowski | CFD Research Corporation, USA |
| Prof. R. Ubar | Tallin Technical University, Estonia |
| Prof. G. Wachutka | Technische Universität München, Germany |
| Prof. K. Wawryn | Technical University of Koszalin, Poland |
| Prof. B. Więcek | Technical University of Lodz, Poland |
| Prof. S. Yoshitomi | Toshiba Corp., Japan |
| Prof. J. Zarebski | Gdynia Maritime University, Poland |
| Dr. M. Zubert | Technical University of Lodz, Poland |

Organizing Committee

| | |
|----------------------|---|
| Prof. A. Napieralski | (Chairman) |
| Dr. M. Orlikowski | (Secretary) |
| Dr. M. Napieralska | (Vice-Chairman) |
| Dr. G. Jabłoński | Department of Microelectronics and Computer Science Technical University of Lodz, Poland |
| | |
| Prof. K. Górecki | Department of Marine Electronics, Gdynia Maritime University, Poland |
| Dr. J. Dąbrowski | |
| Dr. D. Bisewski | |
| | |
| Prof. W. Kuźmicz | Institute of Micro- and Optoelectronics Warsaw University of Technology, Poland |

The MIXDES Conference

The MIXDES conference series started in Dębie near Warsaw in 1994 and has been organised yearly in the most interesting Polish cities. In 2018 we will celebrate the 25th anniversary of the conference in Gdynia.

In short period of time the conference has become an important event in the Central Europe allowing to discuss the recent research progress in the field of design, modelling, simulation, testing and manufacturing in various areas such as micro- and nanoelectronics, semiconductors, sensors, actuators and power devices as well as their interdisciplinary applications.

Areas of Interest

The topics of the Conference include:

- 1. Design of Integrated Circuits and Microsystems**
Design methodologies. Digital and analog synthesis. Hardware-software codesign. Reconfigurable hardware. Hardware description languages. Intellectual property-based design. Design reuse.
- 2. Thermal Issues in Microelectronics**
Thermal and electro-thermal modelling, simulation methods and tools. Thermal mapping. Thermal protection circuits.
- 3. Analysis and Modelling of ICs and Microsystems**
Simulation methods and algorithms. Behavioural modelling with VHDL-AMS and other advanced modelling languages. Microsystems modelling. Model reduction. Parameter identification.
- 4. Microelectronics Technology and Packaging**
New microelectronic technologies. Packaging. Sensors and actuators.
- 5. Testing and Reliability**
Design for testability and manufacturability. Measurement instruments and techniques.
- 6. Power Electronics**
Design, manufacturing, and simulation of power semiconductor devices. Hybrid and monolithic Smart Power circuits. Power integration.
- 7. Signal Processing**
Digital and analogue filters, telecommunication circuits. Neural networks. Artificial intelligence. Fuzzy logic. Low voltage and low power solutions.
- 8. Embedded Systems**
Design, verification and applications.
- 9. Medical Applications**
Medical and biotechnology applications. Biometrics. Thermography in medicine.

Official Language

Official language of the Conference is English, neither translation nor interpretation will be provided.

Call for Papers and Contributions

A call is made for papers, contributions and other conference activities on the topics mentioned above.

Full papers should be submitted till February 28th, 2018 - only in electronic form (MS Word, RTF, Open Office Writer, LaTeX, together with a generated PDF file).

The paper submission form and required format is available on our Web page. Authors are asked to indicate the topic into which their papers fall. The papers will be reviewed by at least two referees from the International Programme Committee. The papers will be published in the proceedings from the author's electronic submission.

Tutorials and Special Sessions - Call for Proposals

Several tutorials/special sessions will be held prior to the conference. Authors willing to propose a tutorial at MIXDES 2018 are invited to send a proposal to the Organizing Committee. The proposal should consist of a three-page summary including tutorial title, name and affiliation of the lecturer(s), tutorial objectives and audience, topical outline and provisional schedule of the tutorial.