



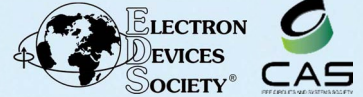
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# CALL FOR PAPERS MIXDES 2015

22<sup>nd</sup> International Conference  
Mixed Design of Integrated  
Circuits and Systems

Toruń, Poland, June 25-27, 2015

<http://www.mixdes.org>



## Organised by:

Department of Microelectronics and Computer Science, Technical University of Łódź, Poland  
Institute of Microelectronics and Optoelectronics, Warsaw University of Technology, Poland  
ROS3D Consortium, Institute of Science and Technology Stipendium  
Military Institute of Aviation Medicine, Poland

## in co-operation with:

Poland Section IEEE - ED & CAS Chapter

Section of Microelectronics and Section of Signals, Electronic Circuits and Systems  
of the Committee of Electronics and Telecommunication of the Polish Academy of Sciences

Commission of Electronics and Photonics of Polish National Committee of International Union of Radio Science

## INVITED PAPERS

### Challenges in Design-oriented Modeling in Biology

*Christophe Lallement (University of Strasbourg, France)*

### Energy Efficiency Optimization for Digital Applications in 28nm UTBB FDSOI Technology

*Amara Amara (ISEP, France)*

### Formal Methods – Support or Scientific Decoration in Software Development?

*Tomasz Szmuc (AGH University of Science and Technology, Poland)*

### Prospect and Outlook of Electrostatic Discharge (ESD) Protection in Emerging Technologies

*Juin J. Liou (University of Central Florida, USA)*

### Silicon PhotoMultipliers: Introducing the Digital Age in Low Light Detection

*Prof. Massimo Caccia (Universita' dell'Insubria, Italy)*

## PAPER SUBMISSION

Full papers should be submitted to the Organising Committee only in electronic form (MS Word, OpenOffice Writer, Rich Text Format, LaTeX or PDF document). The required format is available on the Conference Web page. Authors are asked to indicate the topic into which their papers fall. Papers presented at the conference will be printed in the proceedings directly from the files submitted by the authors.

**Deadline for receipt of papers: March 2<sup>nd</sup>, 2015**

## REGULAR SESSIONS

<i>Design of Integrated Circuits and Microsystems</i>	Design methodologies. Digital and analogue synthesis. Hardware-software codesign. Reconfigurable hardware. Hardware description languages. Intellectual property-based design. Design reuse.
<i>Thermal Issues in Microelectronics</i>	Thermal and electro-thermal modelling, simulation methods and tools. Thermal mapping. Thermal protection circuits.
<i>Analysis and Modelling of ICs and Microsystems</i>	Simulation methods and algorithms. Behavioural modelling with VHDL-AMS and other advanced modelling languages. Microsystems modelling. Model reduction. Parameter identification.
<i>Microelectronics Technology and Packaging</i>	New microelectronic technologies. Packaging. Sensors and actuators.
<i>Testing and Reliability</i>	Design for testability and manufacturability. Measurement instruments and techniques.
<i>Power Electronics</i>	Design, manufacturing and simulation of power semiconductor devices. Hybrid and monolithic Smart Power circuits. Power integration. Wide band gap devices.
<i>Signal Processing</i>	Digital and analogue filters, telecommunication circuits. Neural networks. Artificial intelligence. Fuzzy logic. Low voltage and low power solutions.
<i>Embedded Systems</i>	Design, verification and applications.
<i>Medical Applications</i>	Medical and biotechnology applications. Thermography in medicine.
<i>Student Projects</i>	

# SPECIAL SESSIONS

## Biomedical Measurement Technologies

organized by *Dr. Olaf Truszczyński, Prof. Marek Prost (Military Institute of Aviation Medicine, Poland)*  
and *Prof. Witold Pleskacz (Warsaw University of Technology, Poland)*

## Issues of Stereoscopic 3D Technologies and Image Acquisition in Current Research and Development

organized by *ROS3D Consortium, Institute of Science and Technology Stipendium*

## Open Source Compact/SPICE Modeling

organised by *Dr. Daniel Tomaszewski (Inst. of Electron Techn., Poland)*  
and *Dr. Władysław Grabiński (GMC Suisse, Switzerland)*

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