

## The Venue

We would like to invite you to Gdańsk, a city located at the Bay of Gdansk, at southern shores of the Baltic Sea. Gdańsk is the capital and largest city of the Pomeranian Voivodeship. It is Poland's principal seaport with famous Shipyard of Gdansk.

Gdańsk is a city established over one thousand years ago. It has a complex history, having had periods of Polish, German and self rule. An important shipbuilding and trade port since the Middle Ages, in 1361 it became a member of the Hanseatic League which influenced its economic, demographic and urban landscape. It also served as Poland's principal seaport, and was the largest city of Poland in the 15<sup>th</sup> - 17<sup>th</sup> centuries.

The city has some buildings surviving from the time of the Hanseatic League. Most tourist attractions are located along or near Ulica Długa (Long Street) and Długi Targ (Long Market), a pedestrian thoroughfare surrounded by buildings reconstructed in historical (primarily during the 17<sup>th</sup> century) style and flanked at both ends by elaborate city gates.

Today Gdańsk is one of the most significant academic centres in the north of Poland. Among almost 500 thousand inhabitants over 80 thousand of them are students.

For more information see:

<https://visitgdansk.com/en/>  
<https://www.gdansk.pl/en/for-tourists>



Fot. Wikimedia/P.Hughes

## Travel Arrangement

**Air:** The Gdańsk Lech Wałęsa airport (IATA: GDN, ICAO: EPGD) is an international airport located 12 km northwest of Gdańsk city center. A train service and bus lines connect the airport and downtown Gdańsk.

**Rail:** Gdańsk is connected to all major cities in Poland. See <http://rozklad.pkp.pl/en> for most up-to-date connection schedule.

**Coach:** Many travel agencies offer permanent coach connections between Gdańsk and numerous cities in Europe.

**Road:** There are three main routes leading directly to Gdańsk, Sopot and Gdynia: the E28 from the west, the E75 from the south, and the E77 from the east.

## Accommodation

The conference will be held in a hotel located in the city centre. The accommodation will be offered for the Conference participants at the conference site.

## Deadlines

Intended authors should note the following deadlines:

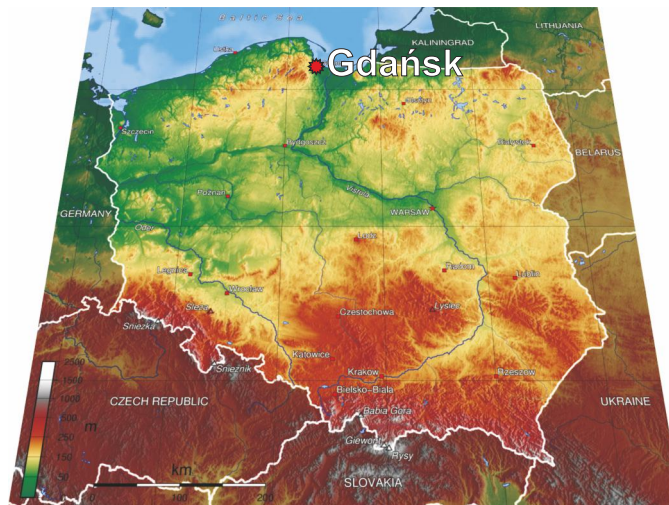
Receipt of papers	March 1 <sup>st</sup> , 2024
Notification of acceptance of papers	April 30 <sup>th</sup> , 2024
Final paper versions and 1-page abstracts	May 15 <sup>th</sup> , 2024

## Price Information

Standard registration fee	390 Euro
IEEE Members registration fee	330 Euro
Student registration fee	260 Euro
Late registration fee (paid after May 15 <sup>th</sup> , 2024)	500 Euro
Accompanying person social events fee	180 Euro

Conference fee includes: participation in all sessions, MIXDES 2024 Proceedings CD and other conference materials, coffee and tea during the breaks, all lunches, the Welcome Party and Conference Banquet, tourist activities (student fee does not include the Welcome Party and the Conference Banquet).

Conference fee does not include accommodation and transport.



## Enquiries

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Department of Microelectronics and Computer Science (K-25)  
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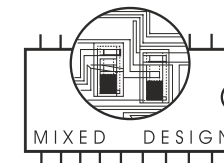
e-mail: [mixdes2024@dmcs.p.lodz.pl](mailto:mixdes2024@dmcs.p.lodz.pl)

## WWW

Information on MIXDES 2024 will be available on the World Wide Web at the following location:

<http://www.mixdes.org>

# CALL FOR PAPERS



## MIXDES 2024

## 31<sup>st</sup> International Conference MIXED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS

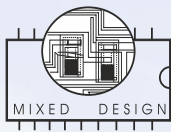


## An invitation to submit paper proposals and attend the Conference



Gdańsk, POLAND, 27 - 29 June 2024





# MIXDES 2024

## 31<sup>st</sup> International Conference MIXED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS Gdańsk, POLAND, 27 - 29 June 2024

### Organized by:

Department of Microelectronics and Computer Science  
Technical University of Lodz, Poland

Faculty of Electronics, Telecommunications and Informatics  
Gdańsk University of Technology, Poland

Department of Marine Electronics  
Gdynia Maritime University, Poland

Institute of Micro- and Optoelectronics  
Warsaw University of Technology, Poland

### In cooperation with:

Poland Section IEEE - CAS & ED Chapters

Section of Microelectronics and Electron Technology  
of the Committee of Electronics and Telecommunication  
of the Polish Academy of Sciences

Commission of Electronics and Photonics  
of Polish National Committee  
of International Union of Radio Science - URSI

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Lodz University of Technology, Poland  
University of Magdeburg, Germany  
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Lodz University of Technology, Poland  
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Warsaw University of Technology, Poland  
Warsaw University of Technology, Poland  
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### Organizing Committee

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Department of Microelectronics and Computer Science  
Technical University of Lodz, Poland

Faculty of Electronics, Telecommunications and Informatics  
Gdańsk University of Technology, Poland

Department of Marine Electronics  
Gdynia Maritime University, Poland  
Institute of Micro- and Optoelectronics  
Warsaw University of Technology, Poland

### The MIXDES Conference

The MIXDES conference series started in Dębe near Warsaw in 1994 and has been organised yearly in the most interesting Polish cities. In 2024 we would like to continue the tradition of inviting you to the most attractive places in Poland and the conference will take place in Gdańsk.

In short period of time the conference has become an important event in the Central Europe allowing to discuss the recent research progress in the field of design, modelling, simulation, testing and manufacturing in various areas such as micro- and nanoelectronics, semiconductors, sensors, actuators and power devices as well as their interdisciplinary applications.

### Areas of Interest

The topics of the Conference include:

- 1. Design of Integrated Circuits and Microsystems**  
Design methodologies. Digital and analog synthesis. Hardware-software codesign. Reconfigurable hardware. Hardware description languages. Intellectual property-based design. Design reuse.
- 2. Thermal Issues in Microelectronics**  
Thermal and electro-thermal modelling, simulation methods and tools. Thermal mapping. Thermal protection circuits.
- 3. Analysis and Modelling of ICs and Microsystems**  
Simulation methods and algorithms. Behavioural modelling with VHDL-AMS and other advanced modelling languages. Microsystems modelling. Model reduction. Parameter identification.
- 4. Microelectronics Technology and Packaging**  
New microelectronic technologies. Packaging. Sensors and actuators.
- 5. Testing and Reliability**  
Design for testability and manufacturability. Measurement instruments and techniques.
- 6. Power Electronics**  
Design, manufacturing, and simulation of power semiconductor devices. Hybrid and monolithic Smart Power circuits. Power integration.
- 7. Signal Processing**  
Digital and analogue filters, telecommunication circuits. Neural networks. Artificial intelligence. Fuzzy logic. Low voltage and low power solutions.
- 8. Embedded Systems**  
Design, verification and applications.
- 9. Medical Applications**  
Medical and biotechnology applications. Biometrics. Thermography in medicine.

### Official Language

Official language of the Conference is English, neither translation nor interpretation will be provided.

### Call for Papers and Contributions

A call is made for papers, contributions and other conference activities on the topics mentioned above.

Full papers should be submitted till March 1<sup>st</sup>, 2024 - only in electronic form (MS Word, RTF, Open Office Writer, LaTeX, together with a generated PDF file).

The paper submission form and required format is available on our Web page. Authors are asked to indicate the topic into which their papers fall. The papers will be reviewed by at least two referees from the International Programme Committee. The papers will be published in the proceedings from the author's electronic submission.

### Tutorials and Special Sessions - Call for Proposals

Several tutorials/special sessions will be held prior to the conference. Authors willing to propose a tutorial at MIXDES 2024 are invited to send a proposal to the Organizing Committee. The proposal should consist of a three-page summary including tutorial title, name and affiliation of the lecturer(s), tutorial objectives and audience, topical outline and provisional schedule of the tutorial.